

**REMARKS/ARGUMENTS**

1. Rejection of claims 1, 2, and 4-7 under 35 U.S.C. 103(a):

Claims 1, 2, and 4-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045).

**Response:**

Claim 1 was previously amended to recite that the primary phase selector outputs two consecutive discrete clocks and at least one interpolated clock with a phase between the phases of the two consecutive discrete clocks, according to the select signal. Furthermore, claim 1 specifies that the two consecutive discrete clocks and the interpolated clock have approximately the same frequency as the reference clock and the input data.

Neither the AAPA nor Hendrickson teaches that a primary phase selector outputs two consecutive discrete clocks and at least one interpolated clock with a phase between the phases of the two consecutive discrete clocks, where the two consecutive discrete clocks and the interpolated clock have approximately the same frequency as the reference clock and the input data.

The AAPA does not teach generating an interpolated clock having a phase between the phases of two consecutive discrete clocks.

Hendrickson only teaches interpolating clock signals in paragraph [0026], where he teaches that delayed versions of the multiplied clock are interpolated for creating versions of the multiplied clock with finer steps. However, Hendrickson does not teach interpolating clock signals that have not been multiplied. Although

Hendrickson teaches in paragraph [0006] that the frequency of the recovered clock may change over time, Hendrickson does not teach that the two consecutive discrete clocks and the interpolated clock have approximately the same frequency as the reference clock and the input data.

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Therefore, the applicant submits that the currently amended claim 1 is patentable over the cited prior art, and should be allowed. Claims 2 and 4-7 are dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claims 1, 2, and 4-7 is therefore respectfully requested.

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2. Rejection of claim 3 under 35 U.S.C. 103(a):

Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045), further in view of Lee et al. (US 2002/0085656).

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**Response:**

Claim 3 is dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claim 3 is therefore respectfully requested.

20 3. Rejection of claims 8 and 9 under 35 U.S.C. 103(a):

Claims 8 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045), further in view of Chen (US 5,850,422).

25 **Response:**

Claims 8 and 9 are dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claims 8 and 9 is therefore respectfully requested.

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Amdt. dated December 12, 2007  
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4. Rejection of claim 10 under 35 U.S.C. 103(a):

Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045), further in view of Cranford, Jr. et al. (US 2004/0170244).

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**Response:**

Claim 10 is dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claim 10 is therefore respectfully requested.

10 Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Sincerely yours,

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Note: Please leave a message in my voice mail if you need to talk to me. (The time in D.C. is 13 hours behind the Taiwan time, i.e. 9 AM in D.C. = 10 PM in Taiwan.)